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Disea Electronics Co., LTD

DISEA Customized Display

PRODUCT SPECIFICATIONS

For Customer: _____

☐ : APPROVAL FOR SPECIFICATION

Customer Model No. _____

☐ : APPROVAL FOR SAMPLE

Module No.: *ZW-T055AZH-01* _____

Date : 2021-12-14 _____

Table of Contents

For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT
LZJ			

2. Revision Record

| Date	Rev.No.	Page	Revision Items	Prepared
2018-07-09	**V0**		**The first release**	**jhxie**
2021-12-14	**V1**		**Add timing**	**LZJ**

3. General Specifications

ZW-T055AZH-01 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, TP, a back light unit. The 5.46'' display area contains 1080 x1920 pixels and can display up to 16.7M colors. This product accords with ROHS environmental criterion.

Note 1:   Color tune is slightly changed by temperature and driving voltage.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	Free viewing	O'Clock	
Gray scale inversion direction	-	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	68.04X120.96	mm	
Number of Dots	1080×RGB×1920	dots	
Controller	HX8399C	-	
Power Supply Voltage	2.8	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	14-LEDs (white)	pcs	
Weight	---	g	
Interface	MIPI	-	

4.Outline.Drawing
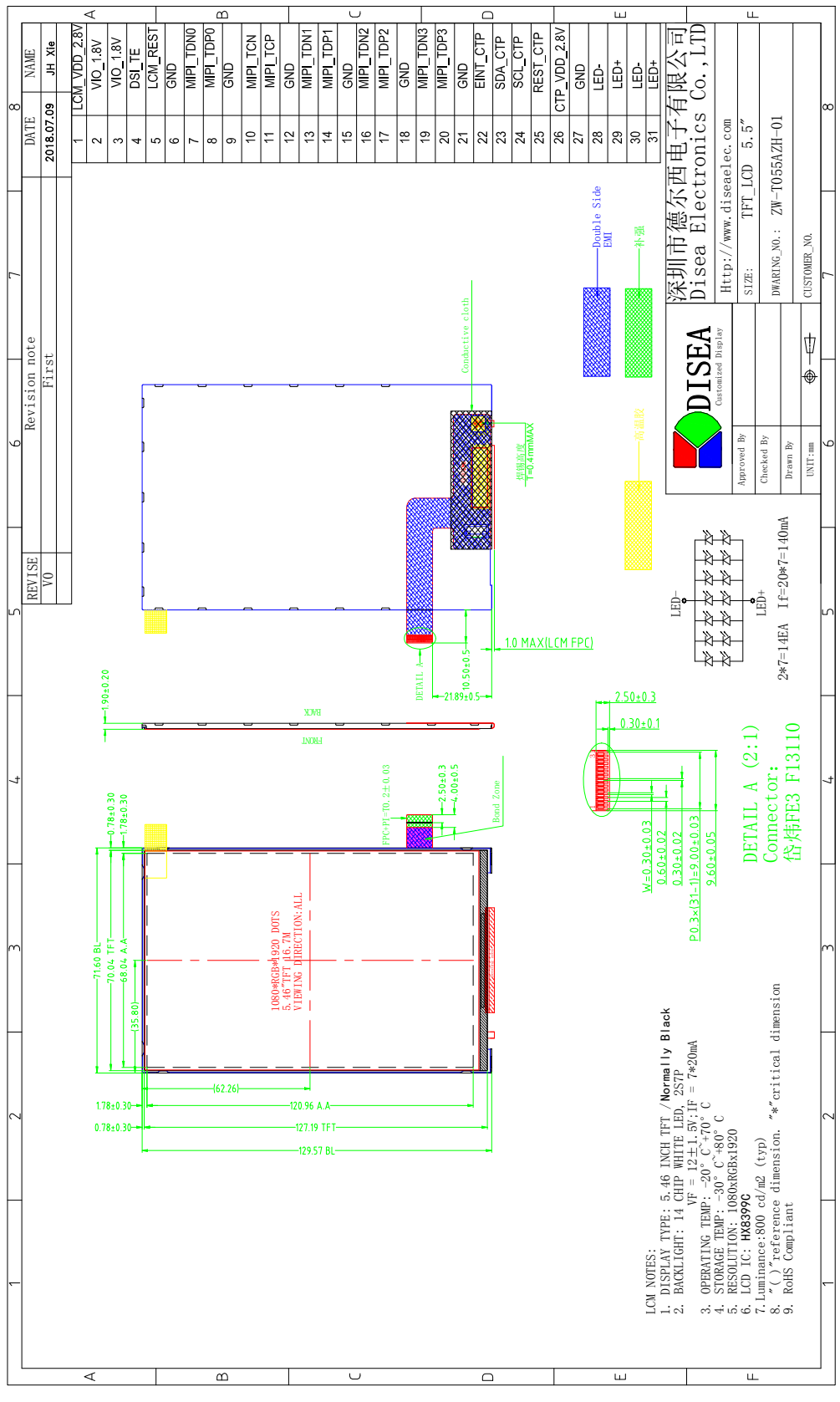
5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	-0.3	3.6	V	1，2
Logic Signal Input /Output Voltage	IOV_{CC}	-0.3	3.3	V	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2. $V_{DD} > V_{SS}$ must be maintained.

3. Please be sure users are grounded when handing LCD Module.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	80℃	-20℃	70℃	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40℃:85%RH MAX.

Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

6. Electrical Specifications

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply		V_{DD}	Ta=25℃	2.6	2.8	3.6	V	
		IOV_{CC}		1.65	1.8	3.3		
Input voltage	'H'	V_{IH}	VDD=2.8V	$0.7V_{VDD}$	-	V_{VDD}	V	
	'L'	V_{IL}	VDD=2.8V	0	-	$0.3V_{VDD}$	V	
Current Consumption		I_{CC1}	Normal mode	-	-	-	mA	1
		I_{CC2}	Sleep mode	-	-	-	mA	1
Clock Frequency		FCLK	-	-	-	-	MHz	

Note:1: Tested in 1×1 chessboard pattern.

6.2 LED backlight specification(VSS=0V ,Ta=25℃)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	Vf	If=20X7mA		12		V	
Uniformity	△Bp	If=20X7mA	75			%	
LED life time	-	If=20mA	-	20000	-	Hours	

I_{LED} VS TEMP
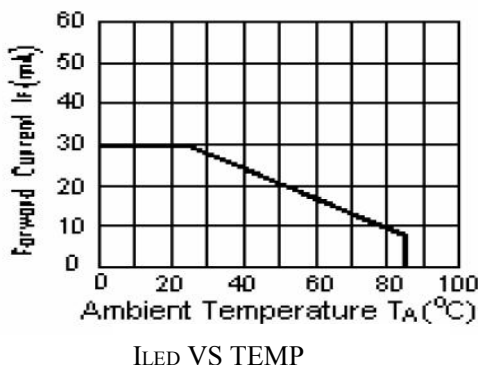
6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	VDD_2.8V	P	Power supply
2-3	VIO_1.8V	P	Digital IO Pad power supply
4	DSI_TE	O	Tearing effect output
5	LCM_RESET	I	Reset the display
6	GND	P	Ground.
7	MIPI_TDN0	I	Negative polarity of low voltage differential data signal
8	MIPI_TDP0	I	Positive polarity of low voltage differential data signal
9	GND	P	Ground.
10	MIPI_TCN	I	Negative polarity of low voltage differential clock signal
11	MIPI_TCP	I	Positive polarity of low voltage differential clock sign
12	GND	P	Ground.
13	MIPI_TDN1	I	Negative polarity of low voltage differential data signal
14	MIPI_TDP1	I	Positive polarity of low voltage differential data signal
15	GND	P	Ground.
16	MIPI_TDN2	I	Negative polarity of low voltage differential data signal
17	MIPI_TDP2	I	Positive polarity of low voltage differential data signal
18	GND	P	Ground.
19	MIPI_TDN3	I	Negative polarity of low voltage differential data signal
20	MIPI_TDP3	I	Positive polarity of low voltage differential data signal
21	GND	P	Ground.
22	EINT_CTP	I	
23	SDA_CTP	I	
24	SCL_CTP	I	Control of the CTP pins
25	RESET_CTP	I	
26	VDD_2.8V	P	
27	GND	P	Ground.
28	LED-	P	LED back light(Cathode)
29	LED+	P	LED back light(Anode)
30	LED-	P	LED back light(Cathode)
31	LED+	P	LED back light(Anode)

6.4 Power on/off sequence
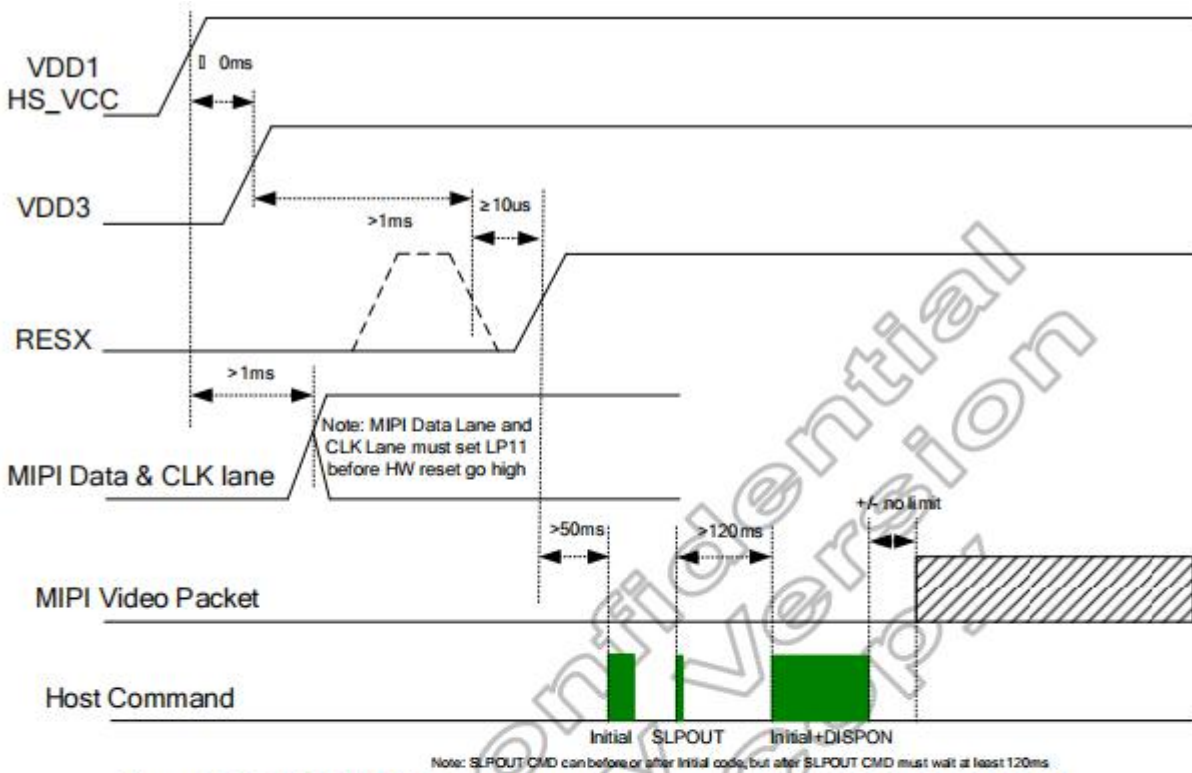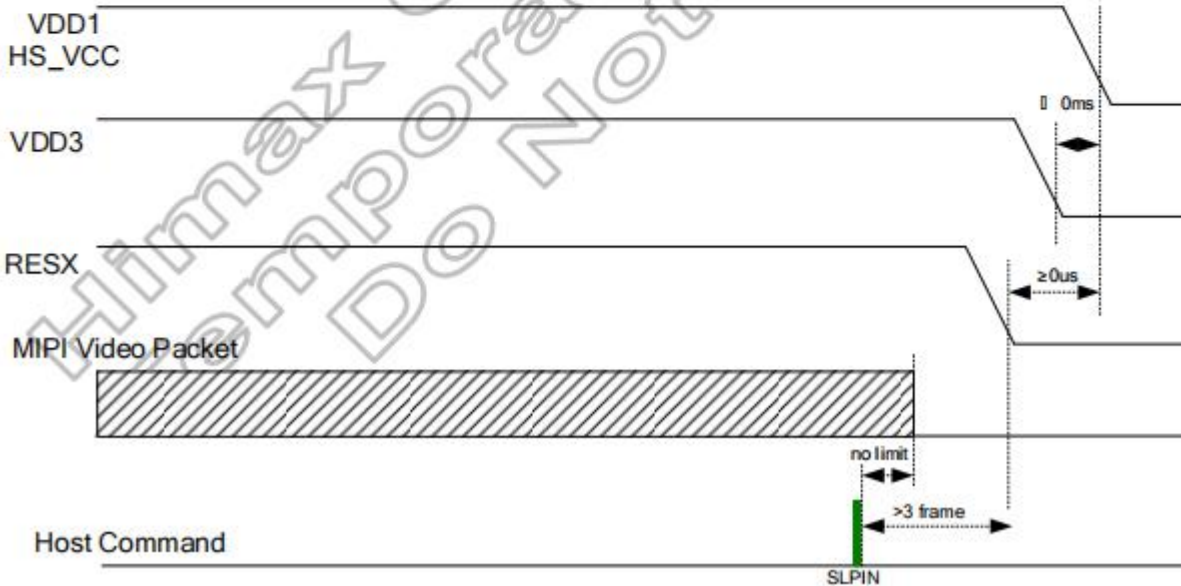

Figure 5.30: VDD3/VDD1 input power on sequence (PCCS[2:0]=000/001/101)
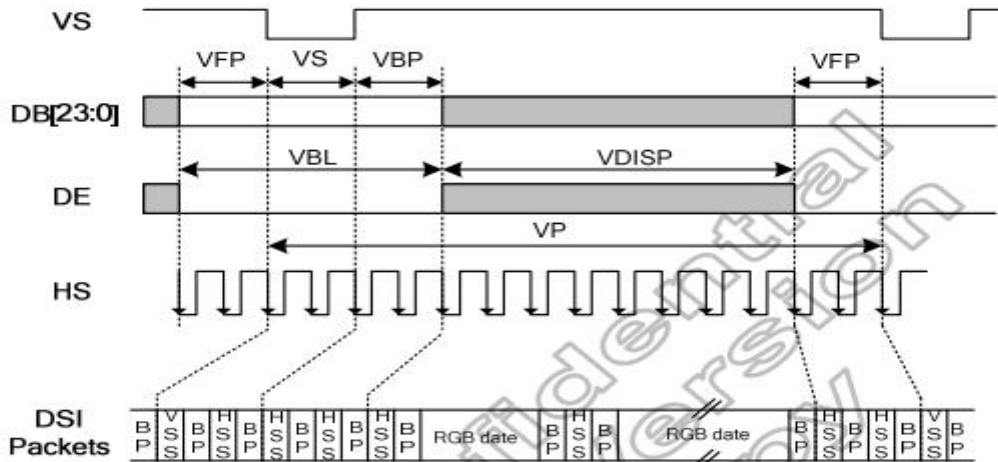
6.5 Timings for DSI video mode

Figure 8.10: Vertical timings for DSI I/F

Vertical Resolution=528+8xNL (VSSA=0V, VDD1=1.8V, VDD3=2.8V, T$_A$=25℃)

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
Vertical cycle	VP	-	534+8xNL	-	-	Line
Vertical low pulse width	VS	-	2	-	Note[1]	Line
Vertical front porch	VFP	-	2	-	-	Line
Vertical back porch	VBP	-	2	-	Note[1]	Line
Vertical data start point	-	VS+VBP	4	-	Note[1]	Line
Vertical blanking period	VBL	VS+VBP+VFP	6	-	-	Line
Vertical active area	-	VDISP	-	528+8xNL	-	Line
Vertical Refresh rate	VRR	-	-	60	-	Hz

Note: (1) The VS and VBP pulse width are related to GSP and GCK timing. The GSP and GCK must be set at corresponding position for LCD normal display.
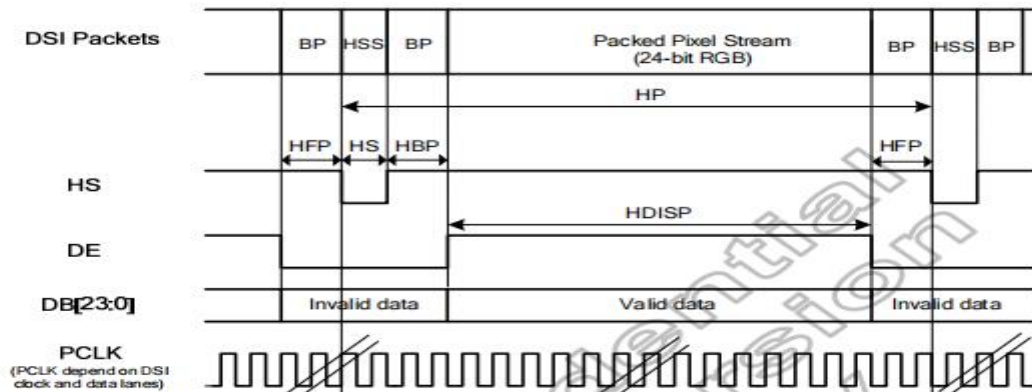

Figure 8.11: Horizontal timing for DSI video mode I/F

Horizontal Resolution=H_RES(1200/1080/1024/960/900/800/720) (VSSA=0V, VDD1=1.8V, VDD3 = HS_VCC =2.8V, T$_A$=25℃)

Parameter	Symbol	Condition	Spec.			Unit
			Min.	Typ.	Max.	
HS cycle	HP	-	H_RES+15	-	-	DCK
HS low pulse width	HS	-	5	-	-	DCK
Horizontal back porch	HBP	-	5	-	-	DCK
Horizontal front porch	HFP	-	5	-	-	DCK
Horizontal data start point	-	HS+HBP	10	-	-	DCK
Horizontal blanking period	HBLK	HS+HBP+HFP	15	-	-	DCK
Horizontal active area	HDISP	-	-	H_RES	-	DCK

7. *Optical Characteristics*

| Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		$\theta=0°$ $\Phi=0°$		800		Cd/m^2	1
Uniformity	△Bp			75		-	%	1,2
Viewing Angle	3:00		Cr≥10	-	80	-	Deg	3
	6:00			-	80	-		
	9:00			-	80	-		
	12:00			-	80	-		
Contrast Ratio	Cr		$\theta=0°$ $\Phi=0°$	1200	1500		-	4
Response Time	T$_r$+T$_f$			-	-	35	ms	5
				-			ms	
Color of CIE Coordinate	W	x	$\theta=0°$ $\Phi=0°$		0.325		-	1,6
		y			0.365		-	
	R	x			0.642		-	
		y			0.336		-	
	G	x			0.295		-	
		y			0.609		-	
	B	x			0.149		-	
		y			0.060		-	
NTSC Ratio	-			65	70	-	%	

Note：The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:
- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

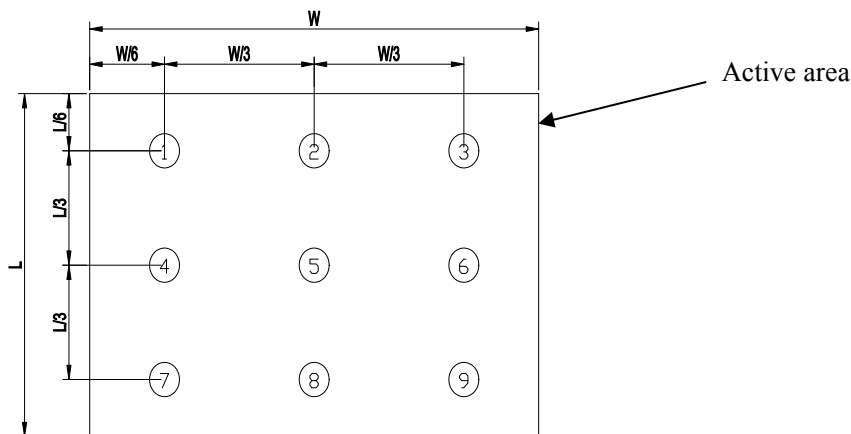Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.
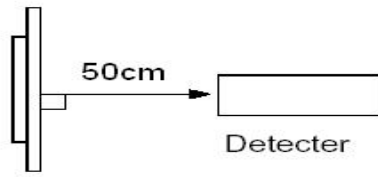
Note 2: The luminance uniformity is calculated by using following formula.

∠Bp = Bp (Min.) / Bp (Max.)×100 (%)
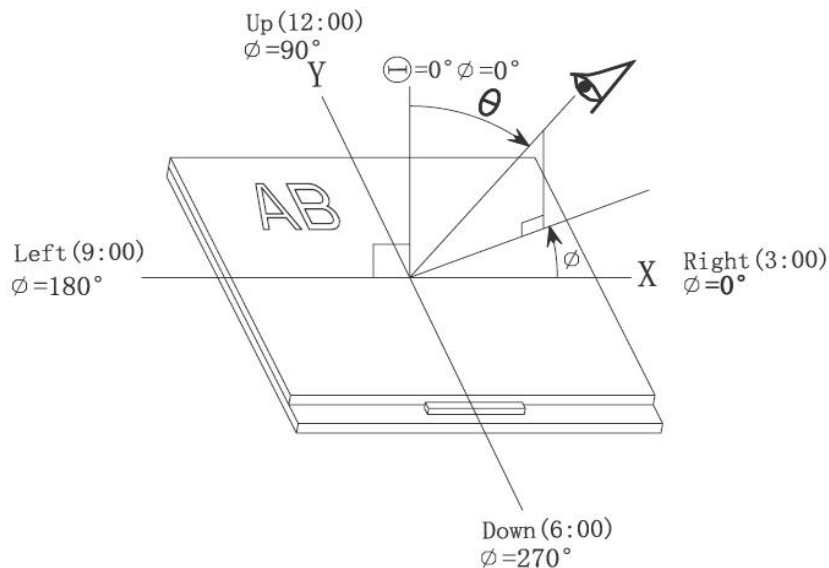
Bp (Max.) = Maximum brightness in 9 measured spots

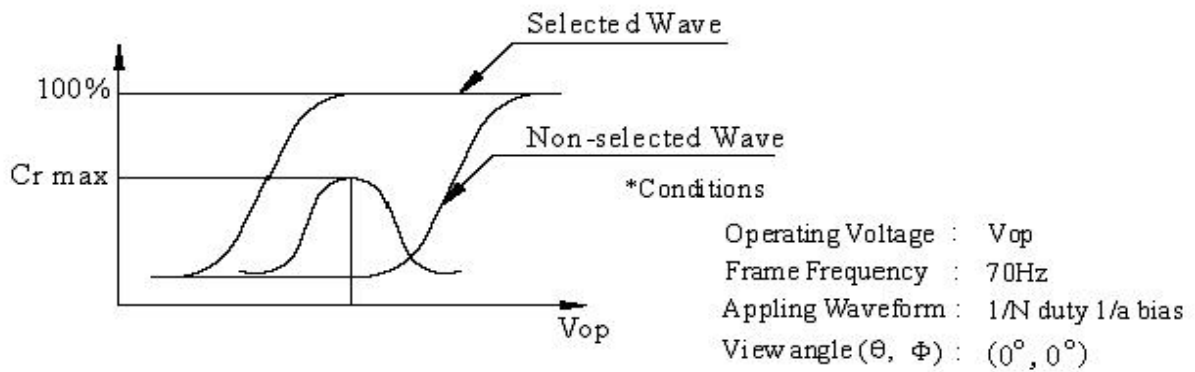Bp (Min.) = Minimum brightness in 9 measured spots.

Note 3: The definition of viewing angle:

Refer to the graph below marked by ϑ and Φ

Note 4: Definition of contrast ratio.(Test LCD using DMS501)

Selected Wave

Non-selected Wave

100%

Cr max

Vop

*Conditions

Operating Voltage : Vop
Frame Frequency : 70Hz
Appling Waveform : 1/N duty 1/a bias
View angle(Θ, Φ) : (0°, 0°)

$$Contrast\ ratio(Cr) = \frac{Brightness\ of\ selected\ dots}{Brightness\ of\ non-selected\ dots}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.

100%
90%

10%

T_r T_f
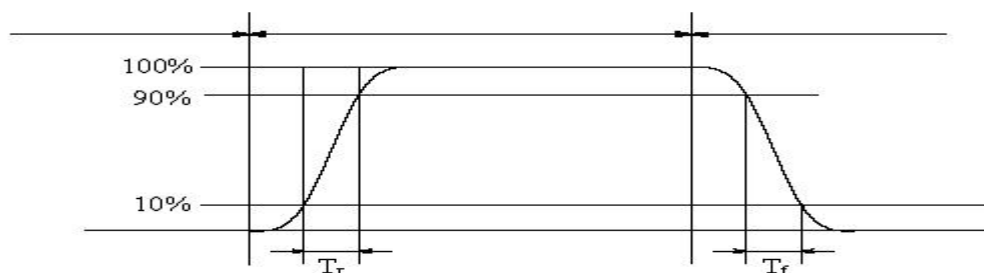
The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.
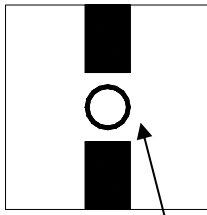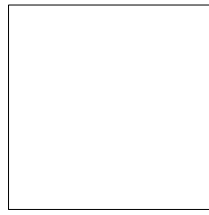

1931 CIE Chromaticity Diagram
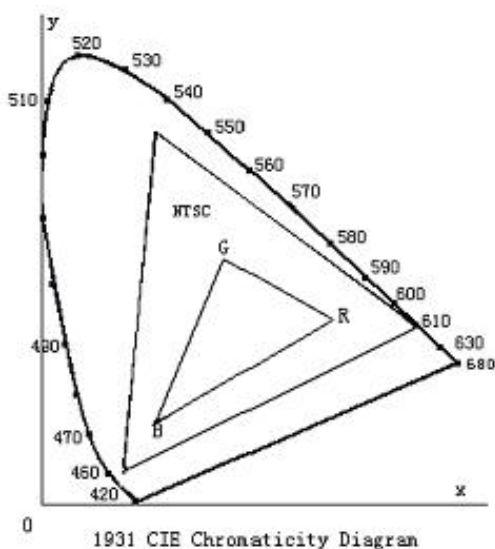
Color gamut:

$$S = \frac{area\ of\ RGB\ triangle}{area\ of\ NTSC\ triangle} \times 100\%$$

Note 7: Definition of cross talk.

*Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100*
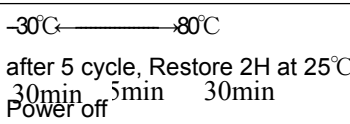
Pattern A   Pattern B

Measurement point(center)

Electric volume value=3F+/-3Hex

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80℃±2℃ 96H Restore 2H at 25℃ Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30℃±2℃ 96H Restore 2H at 25℃ Power off	
3	High Temperature Operation	70℃±2℃ 96H Restore 2H at 25℃ Power on	
4	Low Temperature Operation	-20℃±2℃ 96H Restore 4H at 25℃ Power on	
5	High Temperature/Humidity Storage	60℃±2℃ 90%RH 96H Power off	
6	Temperature Cycle	-30℃ ←———→ 80℃ after 5 cycle, Restore 2H at 25℃ 30min 5min 30min Power off	
7	Vibration Test	10Hz~150Hz, 100m/s2, 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave,300m/s2,11ms	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9. *Precautions for Use of LCD Modules*

9.1 Handling Precautions

9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

   — Isopropyl alcohol   — Ethyl alcohol

   Solvents other than those mentioned above may damage the polarizer.   Especially, do not use the following:

— Water   — Ketone   — Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

9.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

   a. Be sure to ground the body when handling the LCD Modules.

   b. Tools required for assembly, such as soldering irons, must be properly ground.

   c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

   d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.2 Storage precautions

9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :   0℃ ～ 40℃

Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.3 **The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.**

END

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