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Datasheet

LG Display

LM340WW1-SSD1

HD-10-166

SPECIFICATION
FOR
APPROVAL

( ) Preliminary Specification
(●) Final Specification

Title	34.0” WFHD TFT LCD

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LM340WW1
SUFFIX	SSD1

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
/	
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

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Contents

Record of Revisions

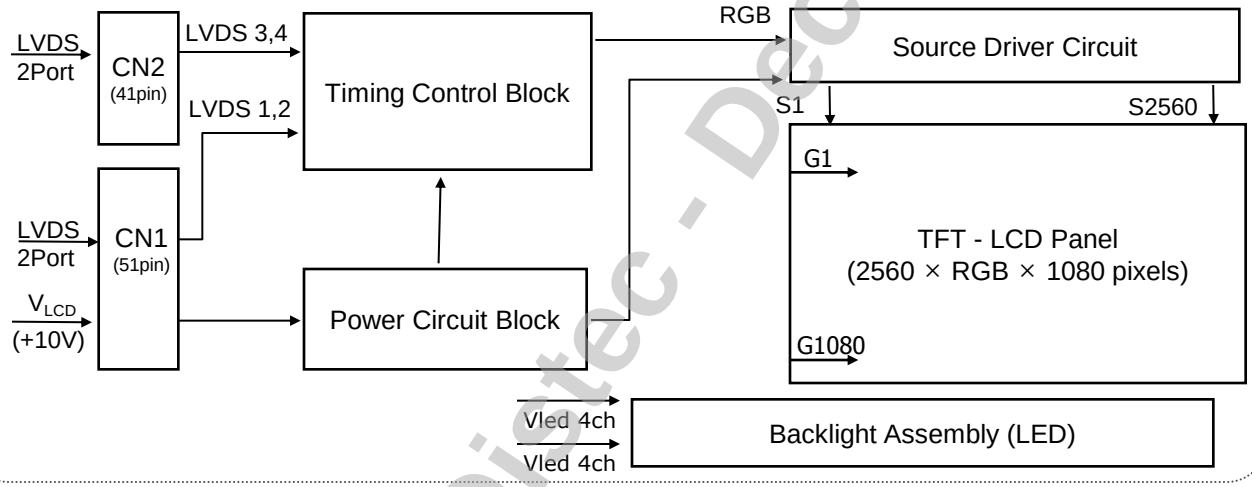
Revision No	Revision Date	Page	Before	After	Application Date
0.0	May. 23. 2019	-	-	First Draft(Preliminary)	May. 23. 2019
0.1	Sep. 24. 2019	22	-	First Draft(Preliminary)	Sep. 24. 2019
1.0	Nov, 26, 2019	-	-	Final Draft	

1. General Description

LM340WW1 is a Color Active Matrix Liquid Crystal Display Light Emitting Diode (White LED) backlight system without LED driver**.** The matrix employs a-Si Thin Film Transistor as the active element.

It is a transmissive type display operating in the normally black mode. It has a 34-inch diagonally measured active display area with Wide Full HD resolution (1080 vertical by 2560 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16.78Million colors with A-FRC (Advanced Frame Rate Control). It has been designed to apply the 8Bit 4 port LVDS interface. It is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.

FIG. 1 Block diagram



General Features

Active Screen Size	34 inches (86.704cm) diagonal (Aspect ratio 21:9)
Outline Dimension	820.80 (H) x 361.00 (V) x 18.80 (T) mm (Typ.)
Pixel Pitch	0.312(H) mm x 0.310(V) mm
Pixel Format	2560 horizontal x 1080 vertical Pixels, RGB stripe arrangement
Color Depth	16.78Million colors, 8bit (6bit + A-FRC)
Luminance, White	300 cd/m^2 (Center, 1 point)
Viewing Angle (CR>10)	R/L 178°(Typ.), U/D 178°Typ.)
Power Consumption	Total 19.3W (Typ.) (4.6W @VLCD, 14.8W @ 65mA)
Weight	Typ. : 3960g , Min : 3760g Max : 4160g
Display Operating Mode	Transmissive mode, Normally Black
Panel type	Reverse type
Surface Treatment	Anti-Glare treatment of the front polarizer (Hard coating (3H))

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 2-1. Absolute Maximum Ratings

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Supply Input Voltage	V_{LCD}	-0.3	+11.0	V_{DC}	At 25℃
Operating Temperature	T_{OP}	0	50	℃	1,2,3
Storage Temperature	T_{ST}	-20	60	℃	
Operating Ambient Humidity	H_{OP}	10	90	%RH	
Storage Humidity	H_{ST}	10	90	%RH	
LCM Surface Temperature(Operation)	$T_{surface}$	0	65	℃	1,4

Notes:
1) Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39℃ Max, and no condensation of water.
2) Maximum storage humidity is up to 40℃, 70% RH only for 4 corner light leakage mura.
3) Storage condition is guaranteed under packing condition.
4) LCM surface temperature should be measured under the condition of V_{LCD} = Typ, f_V = 60Hz, T_a = 25℃, no humidity and typical LED string current.

* f_V = Frame frequency
* T_a = Ambient temperature

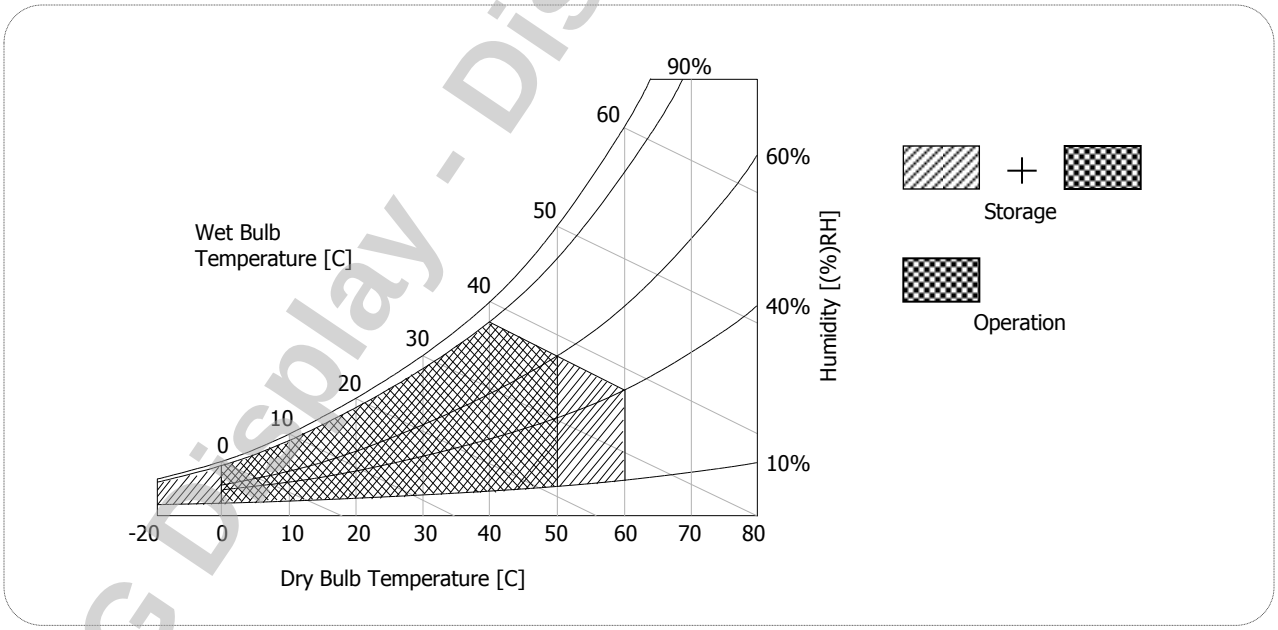


FIG.2 Temperature And Relative Humidity

3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

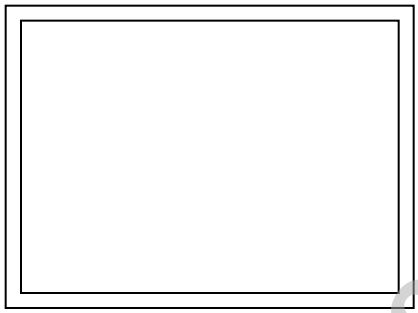
Table 3-1. Electrical Characteristics

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
Module:						
Power Supply Input voltage	V_{LCD}	9.5	10.0	10.5	Vdc	4
Permissive Power Input Ripple	V_{RIPPLE}	-		400	mVp-p	1
Power Supply Input Current	I_{LCD} Typ.	-	460	575	mA	2
	I_{LCD} Max.	-	520	650	mA	
Power Consumption	P_{LCD} Typ.	-	4.60	5.75	Watt	
	P_{LCD} Max.		5.2	6.5	Watt	
Rush Current	I_{RUSH}	-	-	2.0	A	3

Notes:
1) Permissive power ripple should be measured under the condition of V_{LCD} = Typ, 25±2℃, f_V = Max. Refer to page 7 for the pattern and more information.
2) The specified current and power consumption can be measured under the V_{LCD} = Typ, 25±2℃, f_V = 60Hz and the pattern should be changed according to the typical or maximum power condition. The max. current can be measured only with the maximum power pattern. See the page 7 for details.
3) Maximum condition of inrush current: The duration of rush current is about 5ms and rising time of power input is 500us ±20%.(Min).
4) V_{LCD} level must be measured between two points on PCB of LCM V_{LCD}(test point) ~ LCM Ground. (Test condition: Maximum power pattern, 25℃, f_V = 60Hz)

* f_V = Frame frequency

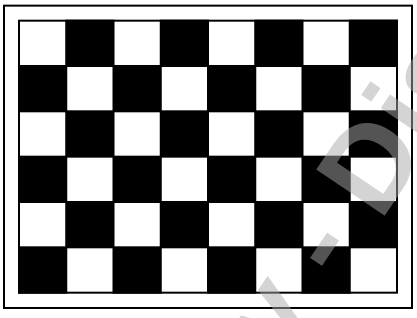
• **Permissive Power Input Ripple**(V_{LCD} = Typ, 25℃, f_V(frame frequency) = Max condition)



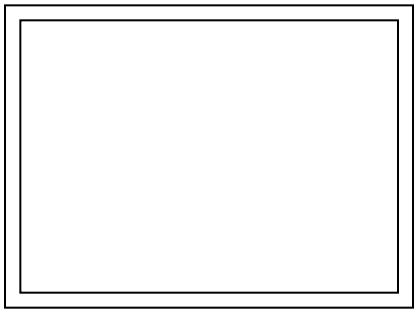
White Pattern

For the exact ripple measurement, the condition of Max 20MHz is recommended in the bandwidth configuration of oscilloscope.

• **Power Consumption**(V_{LCD} = Typ, 25℃, f_V(frame frequency) = 60Hz condition)



Typical Power Pattern



Maximum Power Pattern

FIG.3-1 Mosaic Pattern & White Pattern For Power Consumption Measurement

Table 3-2. LED Bar Electrical Characteristics

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
LED String Current	Is	-	65	70	mA	1,2
LED String Voltage	Vs	52.8	56.8	60.8	V	1,3
Power Consumption	P_{Bar}	-	14.8	15.8	Watt	2,5
LED Life Time	LED_LT	30,000	-	-	Hrs	4

Note: The LED consists of 80 LED packages, 2 strings(parallel) x 20 packages(serial) x 2 bar

Notes:
1) The specified values are for single LED bar.
2) The specified current is defined as the input current for single LED string with 100% duty cycle.
3) The specified voltage is the input LED string voltage at typical current 100% duty cycle.
4) The LED life time is defined as the when brightness of LED itself reach to the 50% of initial value under the conditions at T_a = 25±2℃ and typical LED string current.
5) The power consumption shown above does not include the loss of external LED driver.
The typical power consumption is calculated as P_{bar} = Vs(Typ.) x Is(Typ.) x No. of strings.
The maximum power consumption is calculated as P_{Bar} = Vs(Max.) x Is(Typ.) x No. of strings.

3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(Receptacle) : GT05S-51S-H38 (manufactured by LSC) or equivalent
- Mating Connector : FI-RE51HL(JAE) or equivalent

Table 3-3. Module Connector(CN1) Pin Configuration

No	Symbol	Description
1	GND	Ground
2	NC	No Connection
3	NC	No Connection
4	NC	LGD internal use for I2C
5	NC	LGD internal use for I2C
6	NC	No Connection
7	PBP Select	'H'= PBP Concept , 'L'=normal
8	NC	No Connection
9	NC	No Connection
10	PWM_OUT	Reference signal for LED dimming control
11	GND	Ground
12	R1AN	1st LVDS Channel Signal (A-)
13	R1AP	1st LVDS Channel Signal (A+)
14	R1BN	1st LVDS Channel Signal (B-)
15	R1BP	1st LVDS Channel Signal (B+)
16	R1CN	1st LVDS Channel Signal (C-)
17	R1CP	1st LVDS Channel Signal (C+)
18	GND	Ground
19	R1CLKN	1st LVDS Channel Clock Signal(-)
20	R1CLKP	1st LVDS Channel Clock Signal(+)
21	GND	Ground
22	R1DN	1st LVDS Channel Signal (D-)
23	R1DP	1st LVDS Channel Signal (D+)
24	NC	No Connection
25	NC	No Connection
26	Reserved	No connection or GND
27	Reserved	No connection or GND
28	R2AN	2nd LVDS Channel Signal (A-)
29	R2AP	2nd LVDS Channel Signal (A+)
30	R2BN	2nd LVDS Channel Signal (B-)
31	R2BP	2nd LVDS Channel Signal (B+)
32	R2CN	2nd LVDS Channel Signal (C-)
33	R2CP	2nd LVDS Channel Signal (C+)
34	GND	Ground
35	R2CLKN	2nd LVDS Channel Clock Signal(-)
36	R2CLKP	2nd LVDS Channel Clock Signal(+)
37	GND	Ground
38	R2DN	2nd LVDS Channel Signal (D-)
39	R2DP	2nd LVDS Channel Signal (D+)
40	NC	No Connection
41	NC	No Connection
42	Reserved	No connection or GND
43	GND	Ground
44	GND	Ground (AGP)
45	GND	Ground
46	GND	Ground
47	NC	No connection
48	VLCD	Power Supply +10.0V
49	VLCD	Power Supply +10.0V
50	VLCD	Power Supply +10.0V
51	VLCD	Power Supply +10.0V

Note : PBP = Picture By Picture

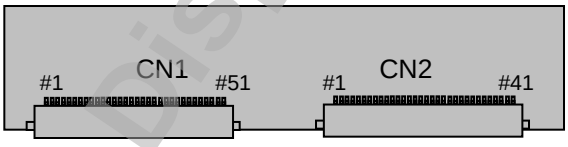
Notes:
1) All GND(ground) pins should be connected together to the LCD module's metal frame.
2) All V_{LCD}(power input) pins should be connected together.
3) All input level of LVDS signals are based on the EIA 644 standard.
4) ITLC is used for image sticking reduction in interlace mode.
(L: Normal mode, H: Interlace image sticking reduction mode)
This pin should be connected to GND in normal mode.
(Low level Input Voltage : GND ~ 0.4V, High level Input Voltage : 1.6 ~ 3.6V)
5) PWM_OUT is a reference signal for LED PWM control. This PWM signal is synchronized with vertical frequency. If the system don't use this pin, do not connect.

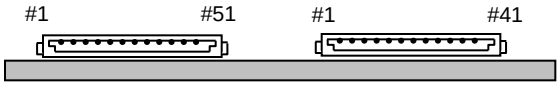
- LCD Connector(CN2) : GT05P-41S-H38 (manufactured by LSC) or equivalent
- Mating Connector : FI-RE41HL(JAE) or equivalent

Table 3-3-1. Module Connector(CN2) Pin Configuration

No	Symbol	Description
1	NC	No connection
2	NC	No connection
3	NC	No connection
4	NC	No connection
5	NC	No connection
6	NC	No connection
7	NC	No connection
8	NC	No connection
9	GND	Ground
10	RA3N	3rd LVDS Channel Signal (A-)
11	RA3P	3rd LVDS Channel Signal (A+)
12	RB3N	3rd LVDS Channel Signal (B-)
13	RB3P	3rd LVDS Channel Signal (B+)
14	RC3N	3rd LVDS Channel Signal (C-)
15	RC3P	3rd LVDS Channel Signal (C+)
16	GND	Ground
17	RCLK3N	3rd LVDS Channel Clock Signal(-)
18	RCLK3P	3rd LVDS Channel Clock Signal(+)
19	GND	Ground
20	RD3N	3rd LVDS Channel Signal (D-)
21	RD3P	3rd LVDS Channel Signal (D+)

No	Symbol	Description
22	NC	No Connection
23	NC	No Connection
24	GND	Ground
25	GND	Ground
26	RA4N	4th LVDS Channel Signal (A-)
27	RA4P	4th LVDS Channel Signal (A+)
28	RB4N	4th LVDS Channel Signal (B-)
29	RB4P	4th LVDS Channel Signal (B+)
30	RC4N	4th LVDS Channel Signal (C-)
31	RC4P	4th LVDS Channel Signal (C+)
32	GND	Ground
33	RCLK4N	4th LVDS Channel Clock Signal(-)
34	RCLK4P	4th LVDS Channel Clock Signal(+)
35	GND	Ground
36	RD4N	4th LVDS Channel Signal (D-)
37	RD4P	4th LVDS Channel Signal (D+)
38	NC	No Connection
39	NC	No Connection
40	GND	Ground
41	GND	Ground





[Rear view of LCM]

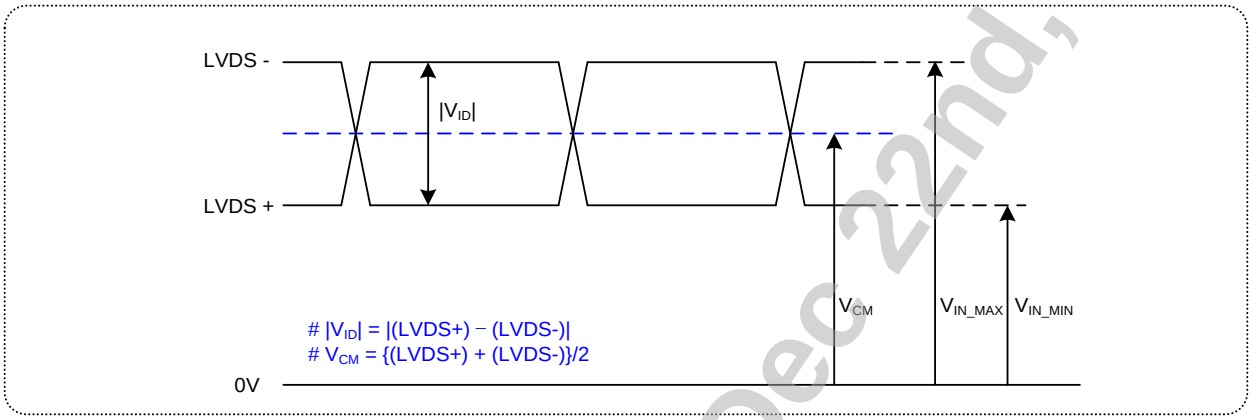
Required signal assignment for flat link(TI:SN75LVDS83) transmitter

No	Pin Name	Required Signal	No	Pin Name	Required Signal
1	VCC	Power supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input(R7)	30	D26	TTL Input(DE)
3	D6	TTL Input(R5)	31	Tx CLKIN	TTL Level clock Input
4	D7	TTL Input(G0)	32	PWR DWN	Power down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input(G1)	34	PLL VCC	Power supply for PLL
7	D9	TTL Input(G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input(G6)	36	LVDS GND	Ground pin for LVDS
9	VCC	Power supply for TTL Input	37	Tx OUT3 +	Positive LVDS differential data output 3
10	D11	TTL Input(G7)	38	Tx OUT3 –	Negative LVDS differential data output 3
11	D12	TTL Input(G3)	39	Tx CLKOUT +	Positive LVDS differential clock output
12	D13	TTL Input(G4)	40	Tx CLKOUT –	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	Tx OUT2 +	Positive LVDS differential data output 2
14	D14	TTL Input(G5)	42	Tx OUT2 –	Negative LVDS differential data output 2
15	D15	TTL Input(B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input(B6)	44	LVDS VCC	Power supply for LVDS
17	VCC	Power supply for TTL Input	45	Tx OUT1 +	Positive LVDS differential data output 1
18	D17	TTL Input(B7)	46	Tx OUT1 –	Negative LVDS differential data output 1
19	D18	TTL Input(B1)	47	Tx OUT0 +	Positive LVDS differential data output 0
20	D19	TTL Input(B2)	48	Tx OUT0 –	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input(B3)	50	D27	TTL Input(R6)
23	D21	TTL Input(B4)	51	D0	TTL Input(R0)
24	D22	TTL Input(B5)	52	D1	TTL Input(R1)
25	D23	TTL Input(RSVD)	53	GND	Ground pin for TTL
26	VCC	Power supply for TTL Input	54	D2	TTL Input(R2)
27	D24	TTL Input(HSYNC)	55	D3	TTL Input(R3)
28	D25	TTL Input(VSYNC)	56	D4	TTL Input(R4)

Notes:
1) Refer to LVDS transmitter data sheet for detail description.
2) 7 means MSB and 0 means LSB at R,G,B pixel data.

3-2-2. LVDS Signal Specifications

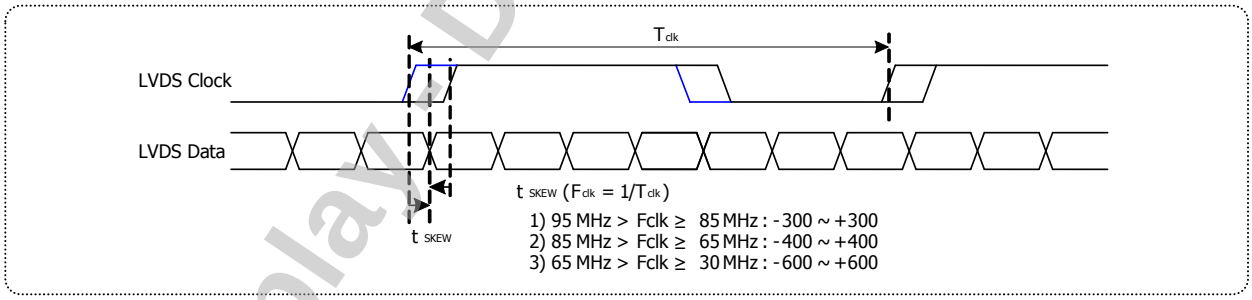
1. DC Specification



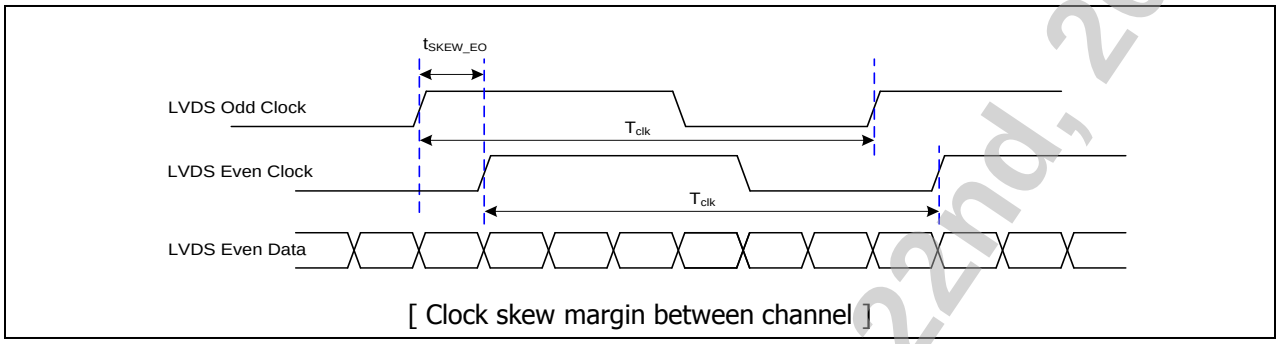
Parameter	Symbol	Min	Max	Unit	Notes
LVDS Differential voltage	$\|V_{ID}\|$	150	600	mV	
LVDS Common mode voltage	V_{CM}	1.0	1.5	V	
LVDS Input voltage range	V_{IN}	0.7	1.8	V	
Change in common mode voltage	ΔVCM	-	250	mV	

Notes : Dose not have any Noise & Peaking in LVDS Signal

2. AC Specification



Parameter	Symbol	Min	Max	Unit	Notes
LVDS Clock to data skew margin	t_{SKEW}	- 300	+ 300	ps	95MHz > Fclk ≥ 85MHz
	t_{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t_{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 30MHz
LVDS Clock to clock skew margin(Even to odd)	t_{SKEW_EO}	- 1/7	+ 1/7	T_{clk}	-

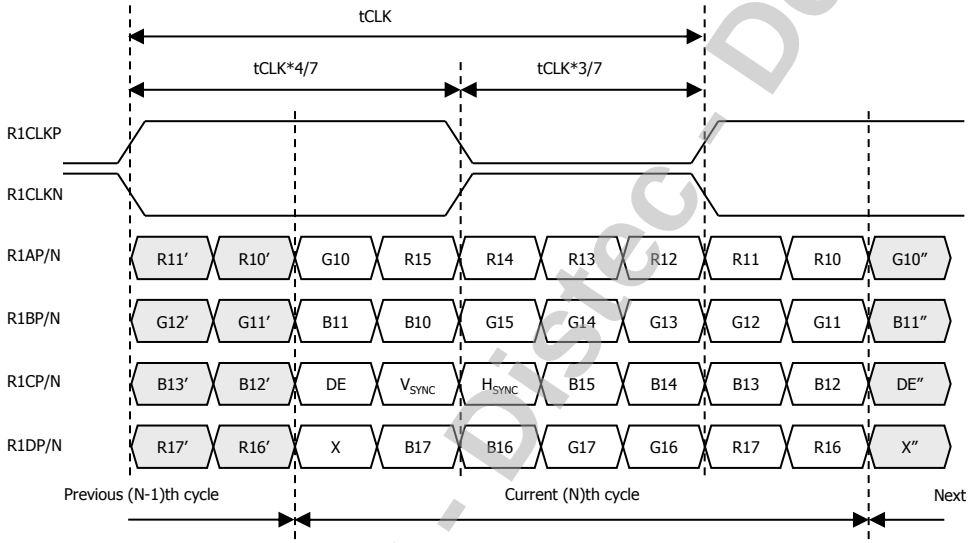


[Clock skew margin between channel]

3. Data Format

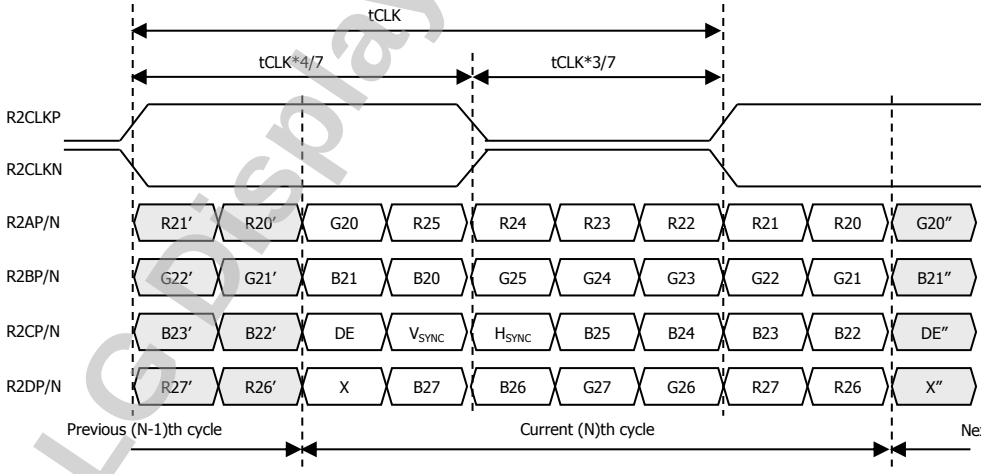
1) LVDS 4 Port (8Bit, VESA)

■ 1st LVDS Channel



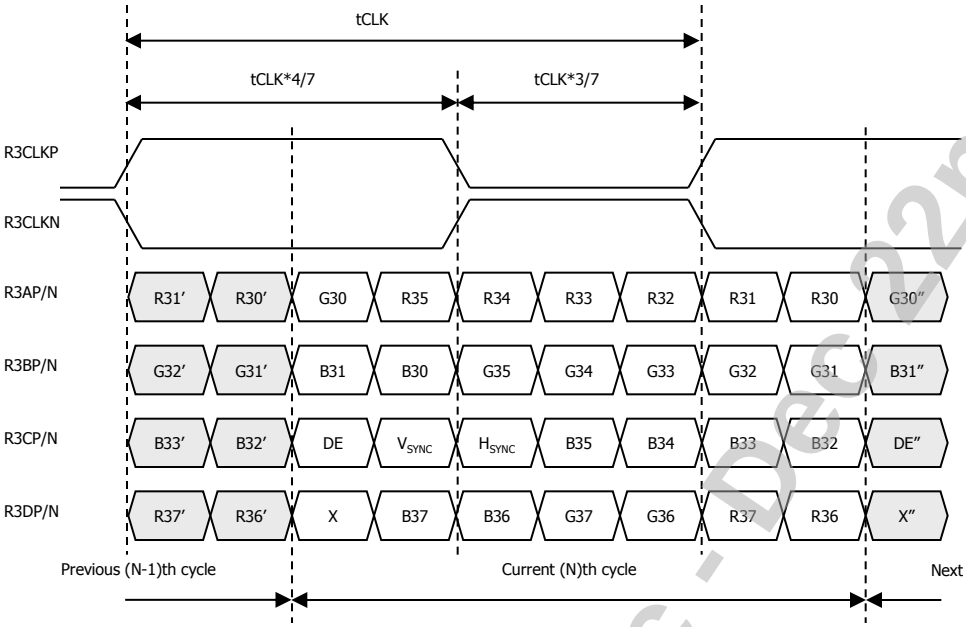
MSB	R17/G17/B17
	R16/G16/B16
	R15/G15/B15
	R14/G14/B14
	R13/G13/B13
	R12/G12/B12
	R11/G11/B11
LSB	R10/G10/B10

■ 2nd LVDS Channel



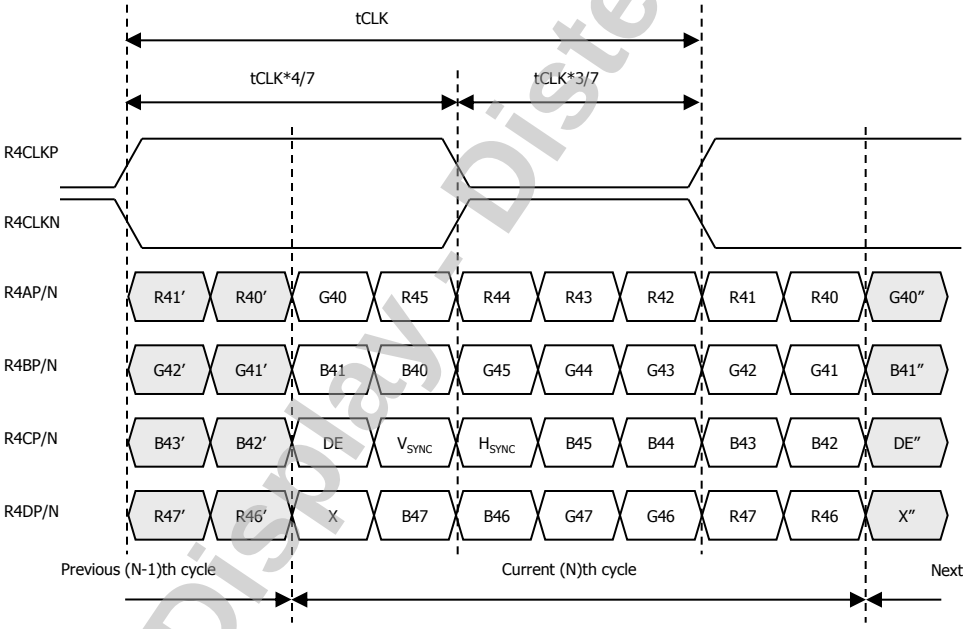
MSB	R27/G27/B27
	R26/G26/B26
	R25/G25/B25
	R24/G24/B24
	R23/G23/B23
	R22/G22/B22
	R21/G21/B21
LSB	R20/G20/B20

■ 3rd LVDS Channel



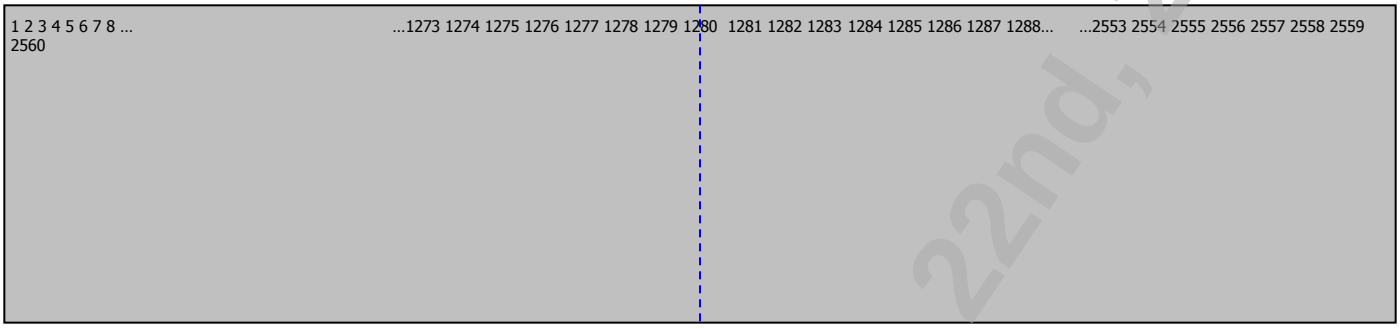
MSB	R37/G37/B37
	R36/G36/B36
	R35/G35/B35
	R34/G34/B34
	R33/G33/B33
	R32/G32/B32
	R31/G31/B31
LSB	R30/G30/B30

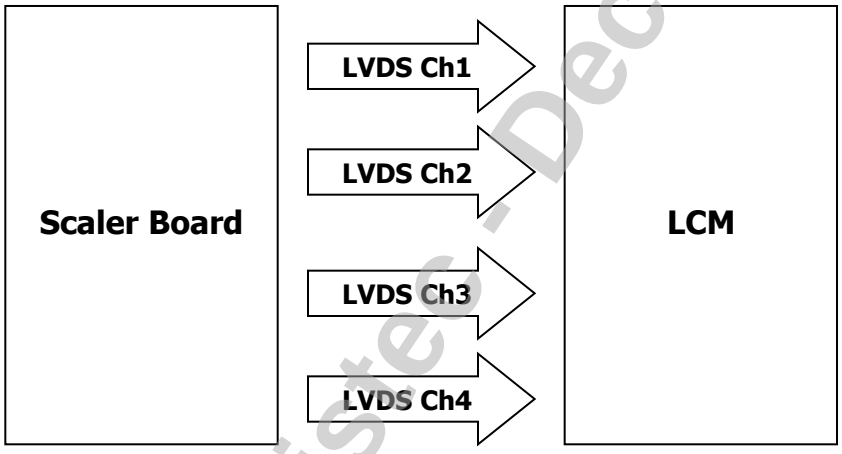
■ 4th LVDS Channel



MSB	R47/G47/B47
	R46/G46/B46
	R45/G45/B45
	R44/G44/B44
	R43/G43/B43
	R42/G42/B42
	R41/G41/B41
LSB	R40/G40/B40

4. LVDS Description of Data Re-Arrange





■ Normal (Single Screen, Pin # 7 of CN1 = Low)

LVDS Ch1 : 1 → 5 → ... 1273 → 1277 → 1281 → 1285 → ... 2553 → 2557
LVDS Ch2 : 2 → 6 → ... 1274 → 1278 → 1282 → 1286 → ... 2554 → 2558
LVDS Ch3 : 3 → 7 → ... 1275 → 1279 → 1283 → 1287 → ... 2555 → 2559
LVDS Ch4 : 4 → 8 → ... 1276 → 1280 → 1284 → 1288 → ... 2556 → 2560

■ PBP (Dual Screen, Pin # 7 of CN1 = High)

LVDS Ch1 : 1 → 3 → 5 → 7 → ... 1273 → 1275 → 1277 → 1279
LVDS Ch2 : 2 → 4 → 6 → 8 → ... 1274 → 1276 → 1278 → 1280
LVDS Ch3 : 1281 → 1283 → 1285 → 1287 → ... 2553 → 2555 → 2557 → 2559
LVDS Ch4 : 1282 → 1284 → 1286 → 1288 → ... 2554 → 2556 → 2558 → 2560

Note : PBP = Picture By Picture

3-2-3. Backlight Connector Pin Configuration

The LED interface connector is a model SM06B-SHJH(HF), wire-locking type manufactured by JST or Equivalent.
The mating connector is a SHJP-06V-S(HF) or SHJP-06-A-K(HF) or Equivalent.
The pin configuration for the connector is shown in the table below.

Table 3-4. LED Connector Pin Configuration

Pin	Symbol	Description	Remark	Pin	Symbol	Description	Remark
1	FB1	Channel1 Current Feedback	Left bar	1	FB1	Channel1 Current Feedback	Right bar
2	NC	No Connection		2	NC	No Connection	
3	VLED	LED Power Supply		3	VLED	LED Power Supply	
4	VLED	LED Power Supply		4	VLED	LED Power Supply	
5	NC	No Connection		5	NC	No Connection	
6	FB2	Channel2 Current Feedback		6	FB2	Channel2 Current Feedback	

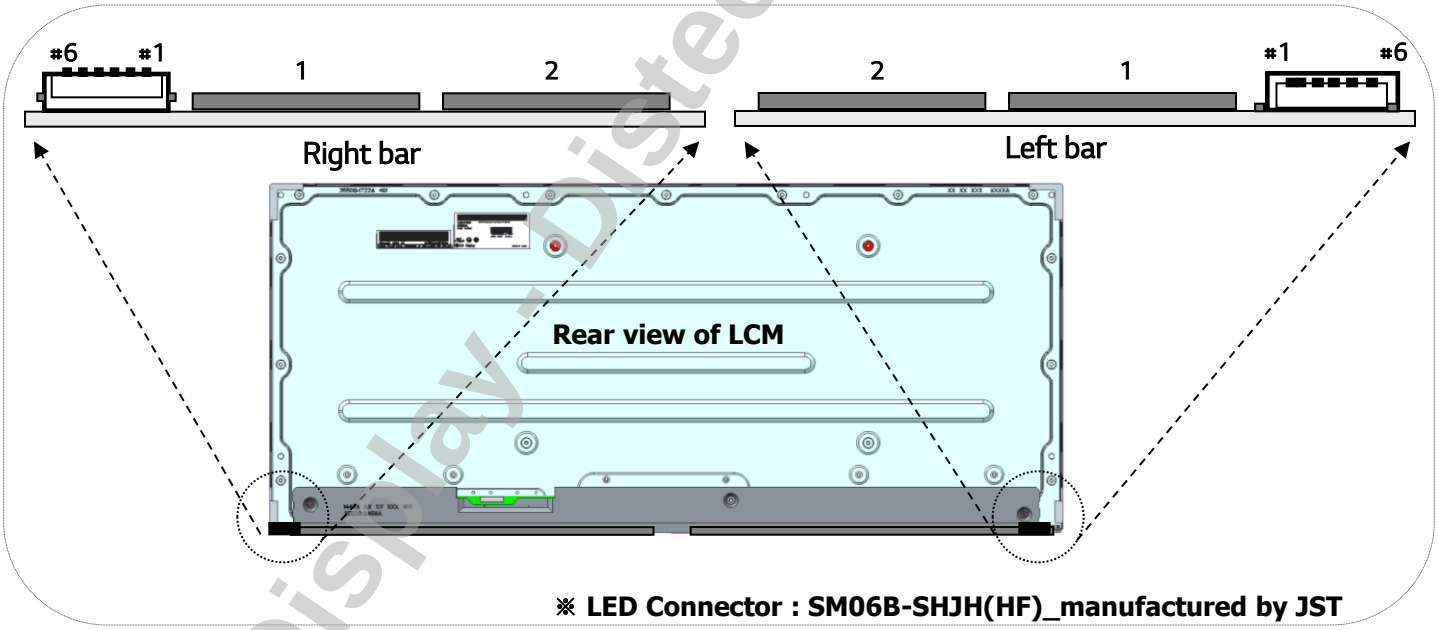


FIG.3-2 Backlight Connector View

3-3. Signal Timing Specifications

This is the signal timing requirement from the signal transmitter. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

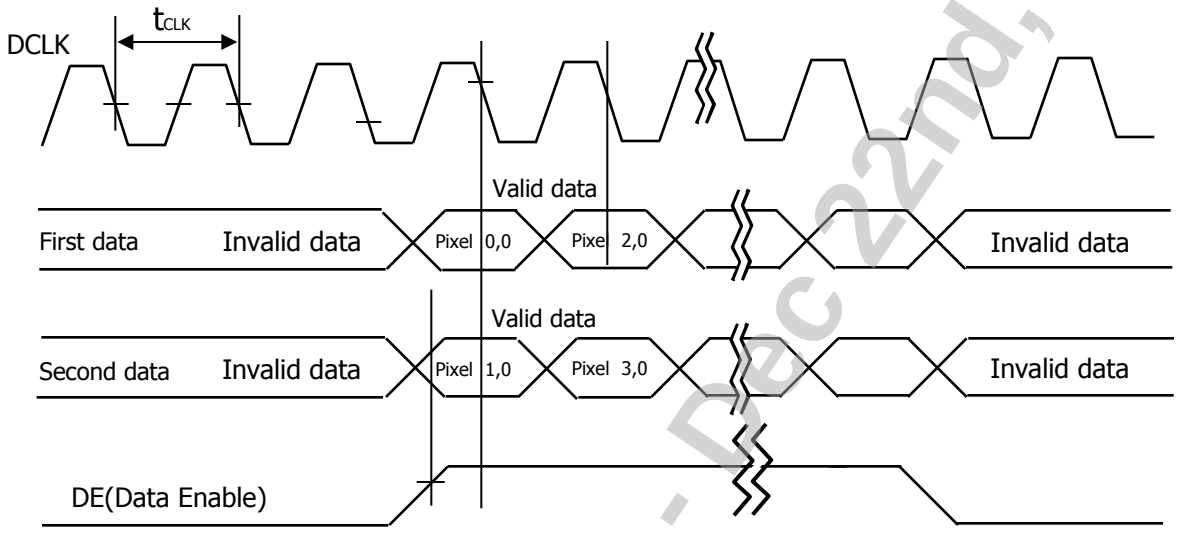
Table 3-5. Timing Table

Parameter		Symbol	Min.	Typ.	Max.	Unit	Notes
D_{CLK}	Period	t_{CLK}	17.2	21.6	25.9	ns	Pixel frequency : Typ. 185.58MHz
	Frequency	f_{CLK}	38.7	46.4	58.0	MHz	
Hsync	Horizontal Valid	t_{HV}	640	640	640	t_{CLK}	
	H Period Total	t_{HP}	688	696	712		
	Hsync Frequency	f_H	55.6	66.7	83.3	kHz	
Vsync	Vertical Valid	t_{VV}	1080	1080	1080	t_{HP}	
	V Period Total	t_{VP}	1102	1111	1330		For D_{CLK}
	Vsync Frequency	f_V	50	60	75	Hz	

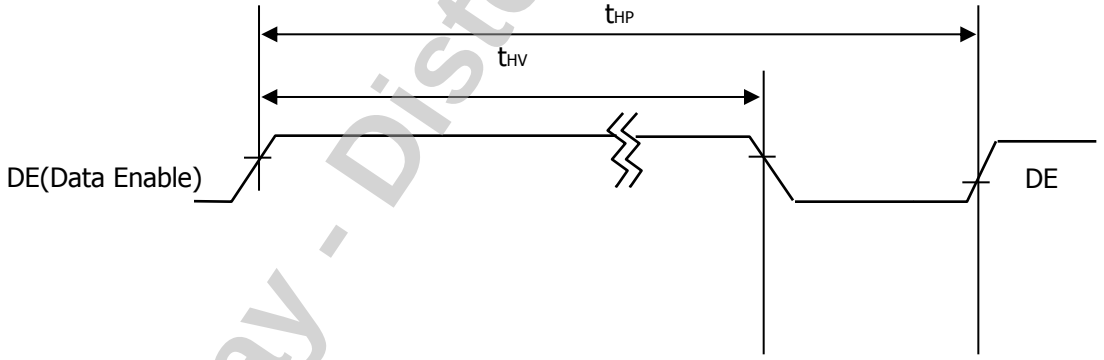
Notes:
1) The value of Hsync Period, Hsync Width and Hsync valid should be even number times of tCLK. If the value is odd number times of tCLK, it can make asynchronous signal timing and cause abnormal display.
2) The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
3) The value of Hsync Period, Hsync Width, and Horizontal Back Porch should be divided by 4 without a remainder.
4) The polarity of Hsync, Vsync is not restricted.

3-4. Signal Timing Waveforms

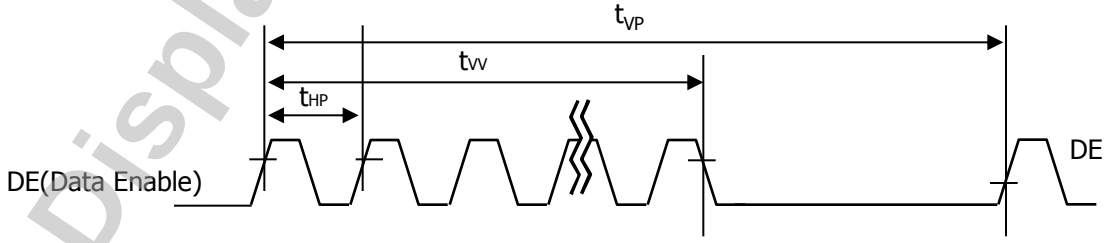
1. DCLK, DE, DATA waveforms



2. Horizontal waveform



3. Vertical waveform



3-5. Color Data Reference

The brightness of each primary color(Red,Green,Blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color.
The table below provides a reference for color versus data input.

Table 3-6. Color Data Reference

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB							LSB	MSB							LSB	MSB							LSB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...					...								...								...			
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...					...								...								...			
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	...					...								...								...			
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

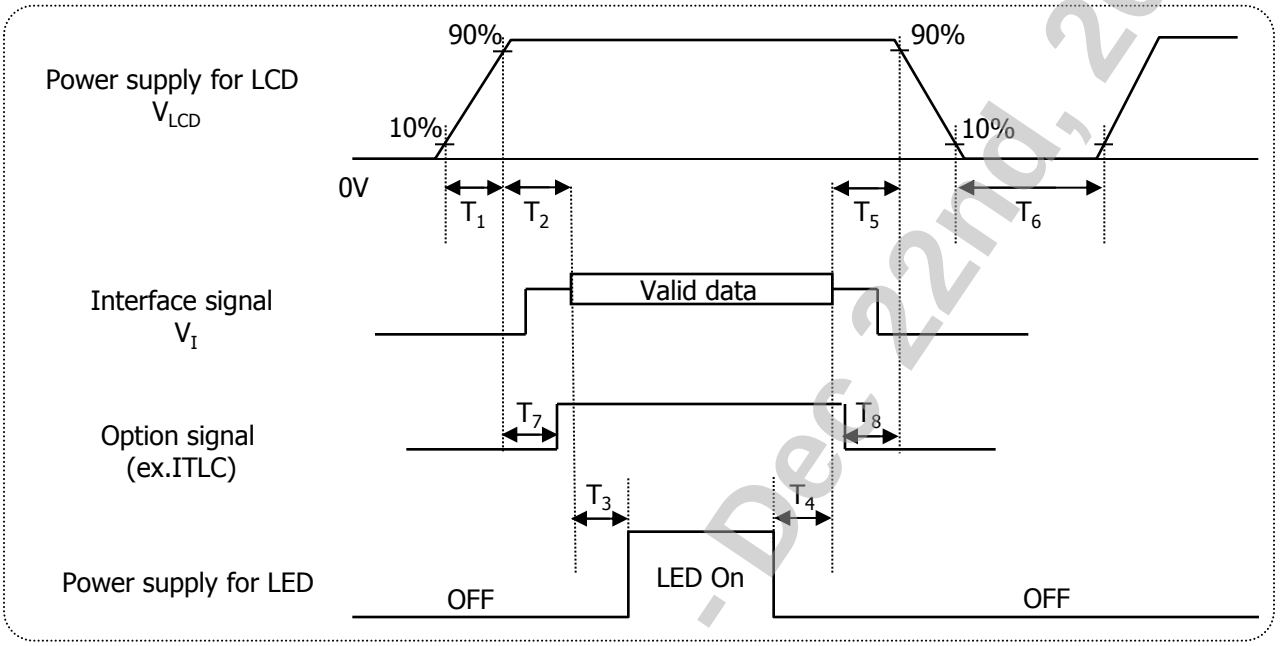


Table 3-7. Power Sequence

Parameter	Values			Units
	Min.	Typ.	Max.	
T_1	0.5	-	10	ms
T_2	0.01	-	50	ms
T_3	500	-	-	ms
T_4	200	-	-	ms
T_5	0.01	-	50	ms
T_6	1000	-	-	ms
T_7	0.5	-	T2	ms
T_8	0	-	-	ms

Notes:
1) Power sequence should be kept all the time including below cases for normal operation.
   - AC/DC Power On/Off
   - Mode change (resolution, frequency, timing, sleep mode, color depth change, etc.)
   The violation of power sequence can cause a significant trouble in display and reliability.
2) Please avoid floating state of interface signal during signal invalid period.
3) When the interface signal is invalid, be sure to pull down the V_{LCD}.(0V)
4) Please turn off the power supply for LED when the level of V_{LCD} changes to prevent noise issue.
5) When measuring valid data starting point, it can be measured that LVDS signal starts swing.

3-7. Power Dip Condition

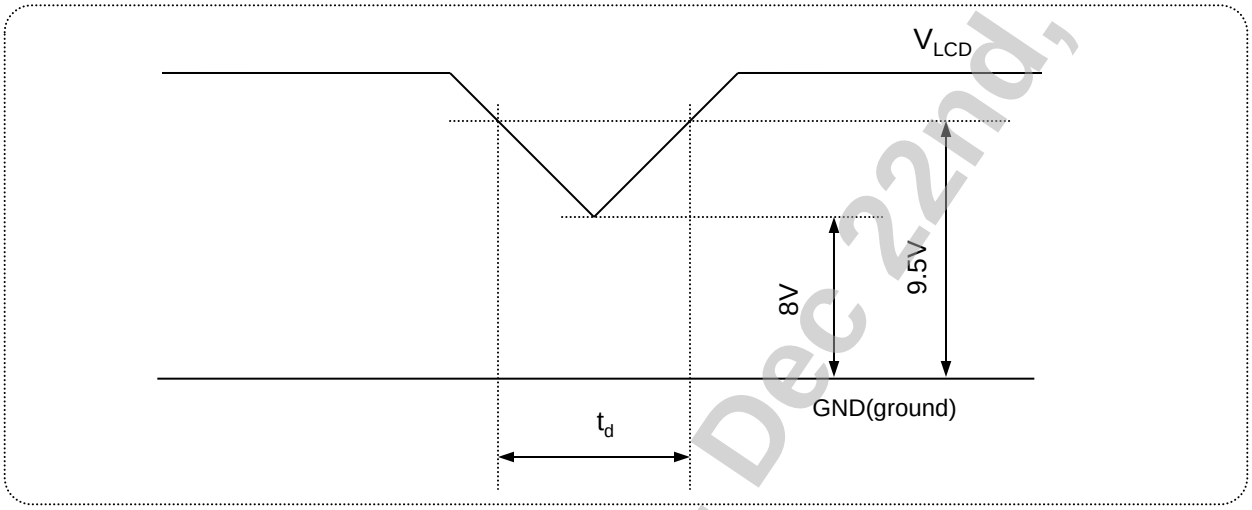


FIG.3-3 Power Dip Condition

For proper operation, stable power supply of V_{LCD} is necessary and power dip is allowed only in below condition. Except this condition, power on/off should follow power sequence specification exactly.

1) Dip Condition

$8V \leq V_{LCD} < 9.5V$, $t_d \leq 20ms$

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25±2℃. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° and aperture 1 degree.
FIG.4-1 presents additional information concerning the measurement equipment and method.

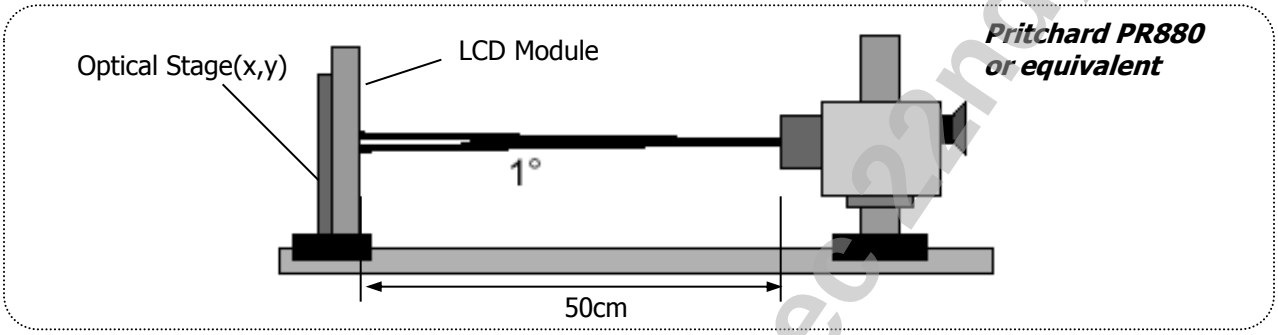


FIG.4-1 Optical Characteristic Measurement Equipment And Method

Table 4-1. Optical Characteristics

(Ta=25 °C, V_{LCD}=10.0V, f_V=60Hz D_{CLK}=185.58MHz, Is=65mA)

Parameter		Symbol	Values			Units	10
			Min	Typ	Max		
Contrast Ratio		CR	700	1000	-		1
Surface Luminance, white		L_{WHITE}	240	300	-	cd/m^2	2
Luminance Variation		δ_{WHITE}	75	-	-	%	3
Response Time	GTG	T_{GTG_AVR}	-	14	28	ms	4
Color Gamut			-	sRGB	-	%	
Color Coordinates [CIE1931] (By PR650)	RED	Rx	Typ -0.03	0.661	Typ +0.03		
		Ry		0.332			
	GREEN	Gx		0.311			
		Gy		0.646			
	BLUE	Bx		0.150			
		By		0.060			
	WHITE	Wx		0.313			
		Wy		0.329			
Color Temperature		-	-	6500	-	K	
Viewing Angle (CR>10,General)	Horizontal	θ_H	170	178	-	Degree	5
	Vertical	θ_V	170	178	-		
Gray Scale				2.2			6

Notes:

1) **Contrast Ratio(CR)** is defined mathematically as: ***(By PR880)***
It is measured at center point(1)

$$\text{Contrast ratio} = \frac{\text{Surface luminance with all white pixels}}{\text{Surface luminance with all black pixels}}$$

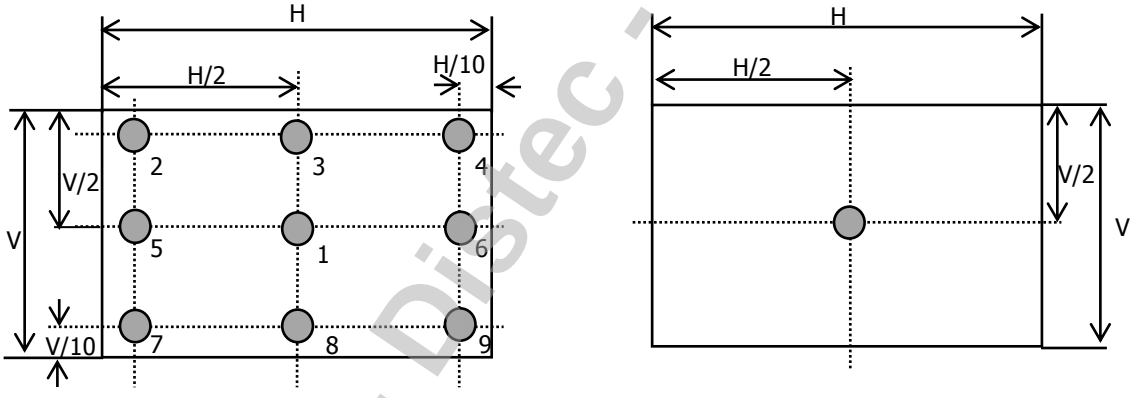
2) **Surface Luminance(L_{WH})** is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.4-1. ***(By PR880)***

3) The **Variation in Surface Luminance** , δ_{WHITE} is defined as: ***(By PR880)***

$$\delta_{WHITE} = \frac{\text{Minimum}(L_{P1}, L_{P2}, \ldots, L_{P9})}{\text{Maximum}(L_{P1}, L_{P2}, \ldots, L_{P9})} \times 100(\%)$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations.
For more information see FIG.4-2.

<Measuring Point For Luminance Variation> **<Measuring Point For Surface Luminance>**



@ H,V: Active Area

FIG.4-2 Measure Point for Luminance

Notes:

4) The **Gray To Gray Response Time** is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ". ***(By RD80S)***
   - Gray step: 5 Step
   - T_{GTG_AVR} is the total average time at rising time and falling time for "Gray To Gray ".
   - For the GTG measurement, the sampling rate of oscilloscope is 500k/s.

Table 4-2. GTG Gray

Gray to Gray		Rising Time				
		G255	G191	G127	G63	G0
Falling Time	G255					
	G191					
	G127					
	G63					
	G0					

Response Time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

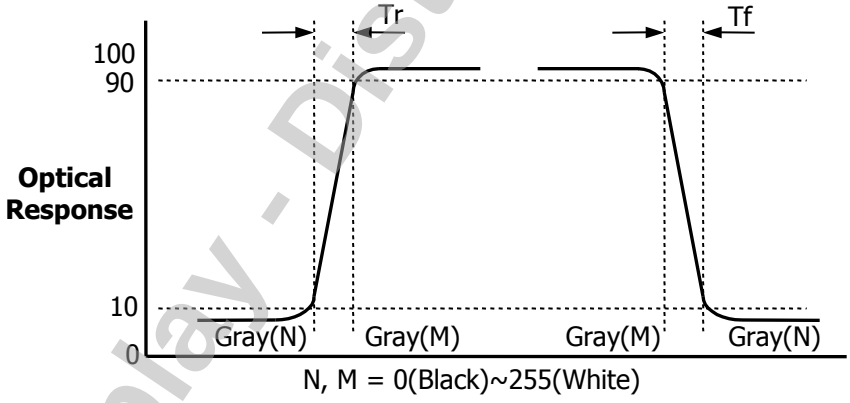


FIG.4-3 Response Time

Notes:

5) **Viewing Angle** is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.4-4. ***(By PR880)***

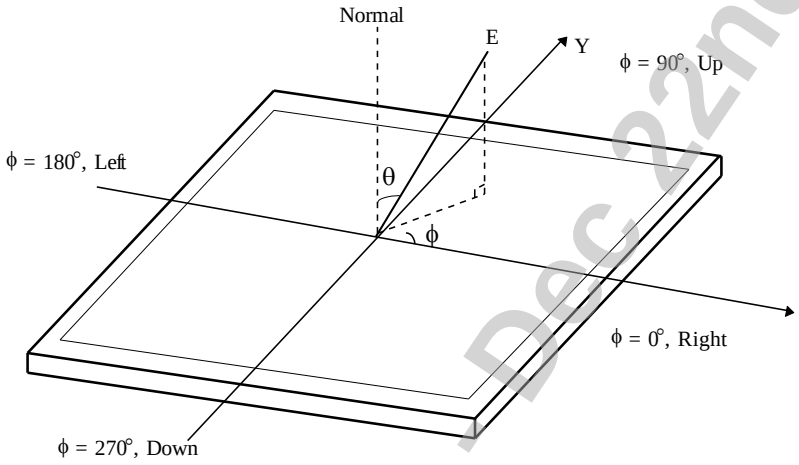


FIG.4-4 Viewing Angle

6) **Gamma Value** is approximately 2.2. For more information see below table.

Table 4-3. Gray Scale Specification

Gray Level	Relative Luminance [%](Typ)
0	0.10
15	0.35
31	1.08
47	1.80
63	3.80
79	6.30
95	10.0
111	15.0
127	20.5
143	27.3
159	34.6
175	42.5
191	51.3
207	61.2
223	72.3
239	85.3
255	100

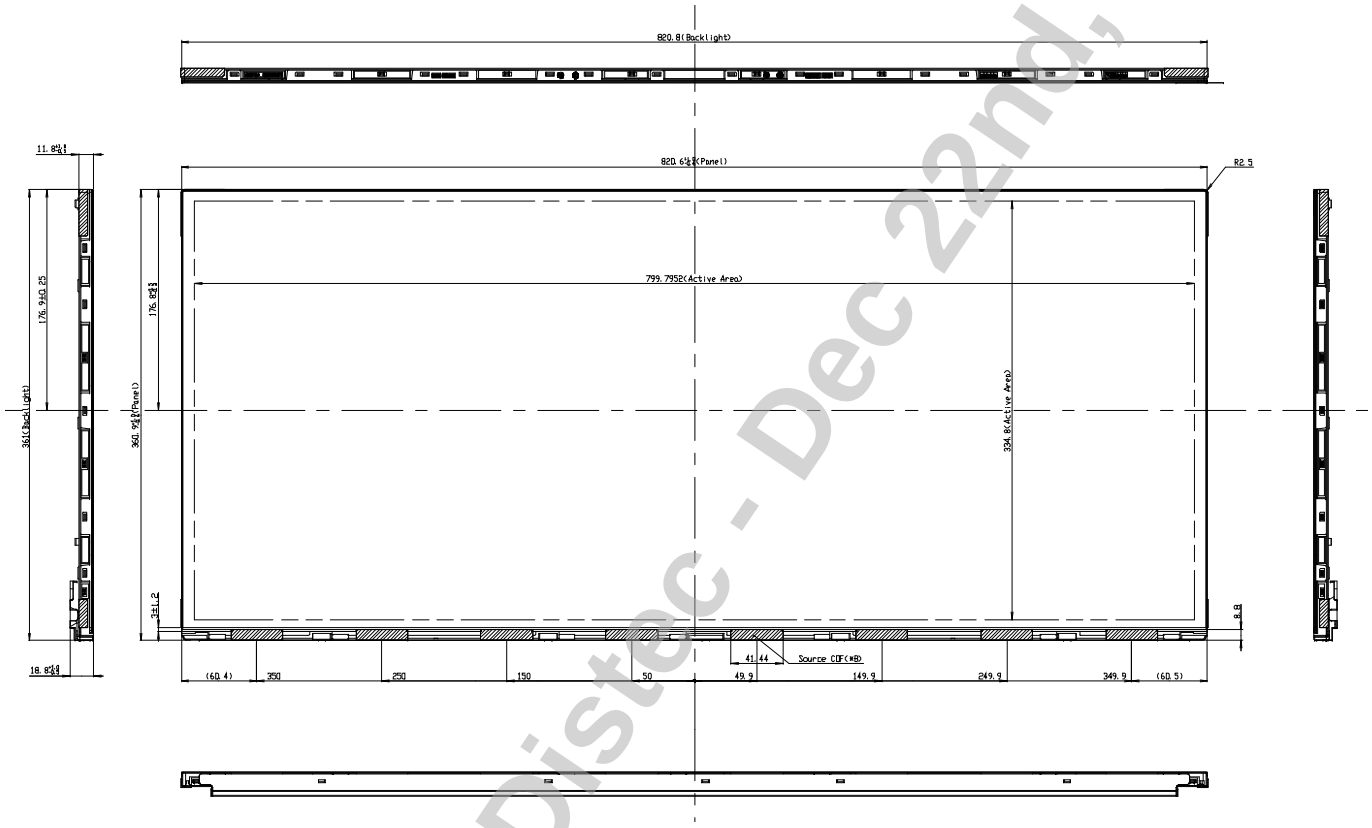
5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

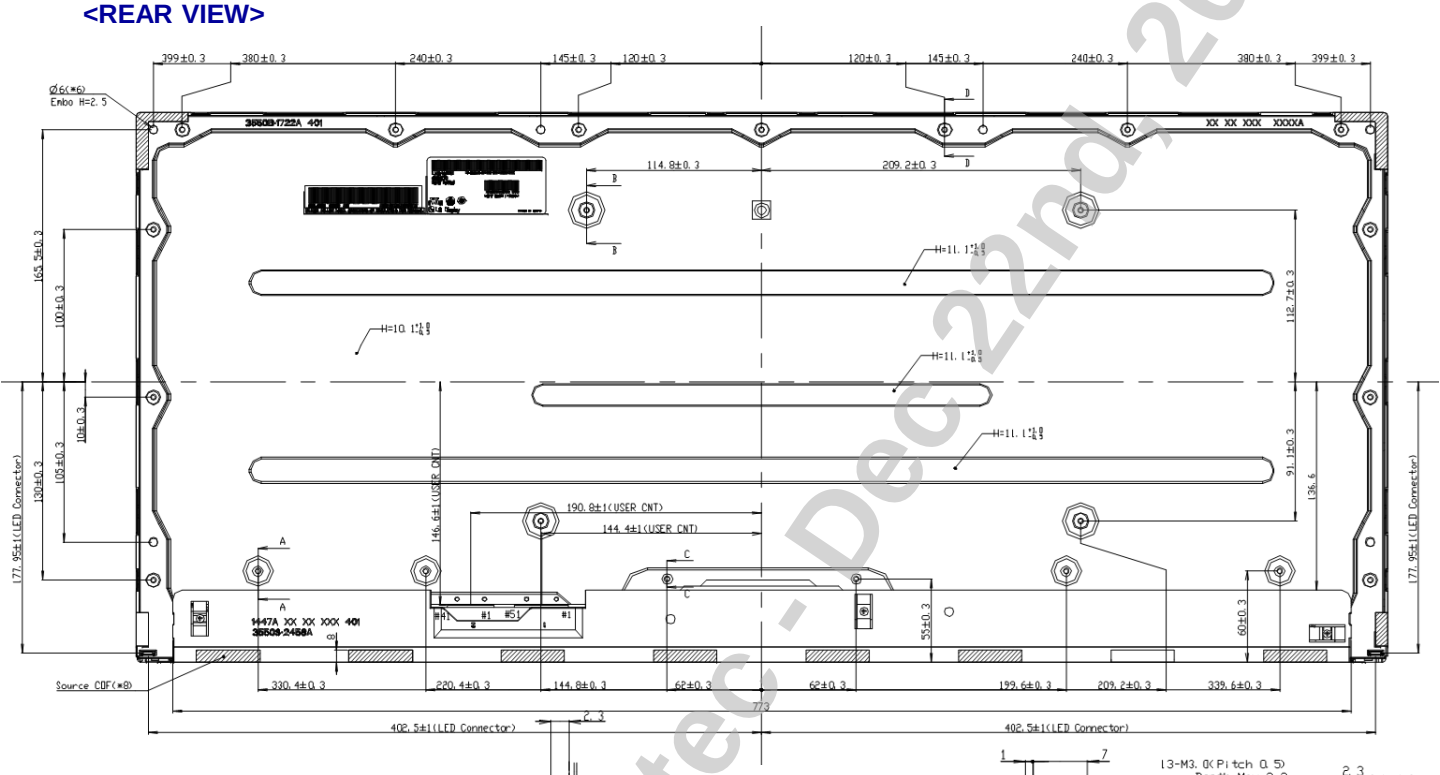
Outline Dimension	Horizontal	820.80mm
	Vertical	361.00mm
	Depth	18.80mm
Bezel Area	Horizontal	-
	Vertical	-
Active Display Area	Horizontal	799.7952 mm
	Vertical	334.800 mm
Weight	Typ. : 3960g , Min : 3760g Max : 4160g	
Surface Treatment	Anti-glare treatment of the front polarizer (Hard coating ,3H)	

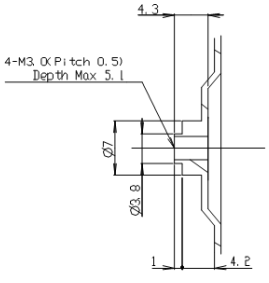
Note: Please refer to a mechanical drawing in terms of tolerance at the next page.
- Outline dimensions (horizontal, vertical and outside depth) are measured by using vernier calipers.
- The inside depth dimensions are measured by using height gauge, when LCM is put face down onto a flat surface.

<FRONT VIEW>

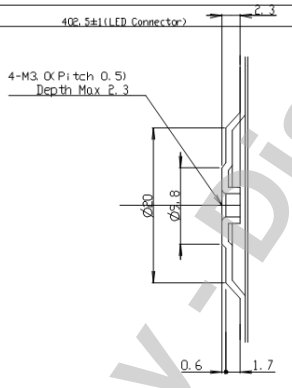


<REAR VIEW>

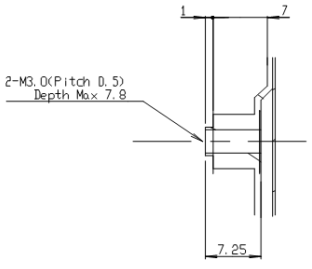




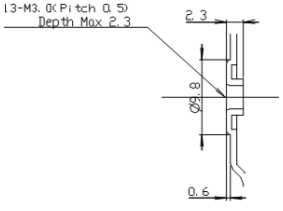
SECTION A-A
SCALE 2/1



SECTION B-B
SCALE 2/1

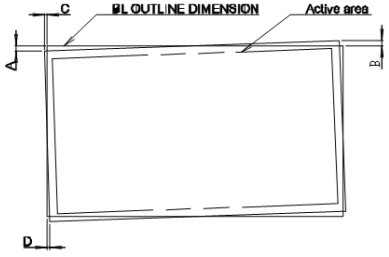


SECTION C-C
SCALE 2/1



SECTION D-D
SCALE 2/1

Notes
1. I/F connector specification
   (1) CN1 : GT05P-51S-H3B(LSM)
   (2) CN2 : GT05P-41S-H3B(LSM)
2. LED connector specification : JST, SM06B-SHJK(HF)
3. Torque of user hole: 3.0~4.0kgf-cm.
4. Tilt and partial disposition tolerance of display area as following
   (1) X-direction : $-0.65 \leq C \leq +0.50$, $-0.65 \leq D \leq +0.50$
   (2) Y-direction : $-0.60 \leq A \leq +0.45$, $-0.60 \leq B \leq +0.45$



5. The LCM warp(warpage) is less than 1.5 on the surface plate.
6. Unspecified tolerance is ±0.5
7. The COF area is weak & senstive, so please don't press the COF area

6. Reliability

Environment test condition

No	Test Item	Condition	Notes
1	High temperature storage test	Ta= 60°C 240h	1
2	Low temperature storage test	Ta= -20°C 240h	1
3	High temperature operation test	Ta= 50°C 50%RH 240h	1
4	Low temperature operation test	Ta= 0°C 240h	1
5	Humidity condition Operation	Ta= 40 °C ,90%RH	1
6	Altitude operating storage / shipment	 0 - 16,400 feet(5,000m) 0 - 40,000 feet(12,192m)	
7	Maximum Storage Humidity for 4 corner light leakage Mura.	Max 70%RH , Ta=40℃	

Note 1) Result Evaluation Criteria:
TFT-LCD panels test should take place after cooling enough at room temperature.
In the standard condition, there should be no particular problems that may affect the display function.

* T_a= Ambient Temperature

7. International Standards

7-1. Safety

a) UL 60950-1, Underwriters Laboratories Inc.
Information Technology Equipment - Safety - Part 1: General Requirements.
b) CAN/CSA C22.2 No.60950-1-07, Canadian Standards Association.
Information Technology Equipment - Safety - Part 1: General Requirements.
c) EN 60950-1, European Committee for Electro-technical Standardization(CENELEC).
Information Technology Equipment - Safety - Part 1: General Requirements.
d) IEC 60950-1, The International Electro-technical Commission(IEC).
Information Technology Equipment - Safety - Part 1: General Requirements.

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C: Size(Inch)
D: Year
E: Month
F ~ M: Serial No.

Notes:

1) Year

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	A	B	C	D	E	F	G	H	J	K

2) Month

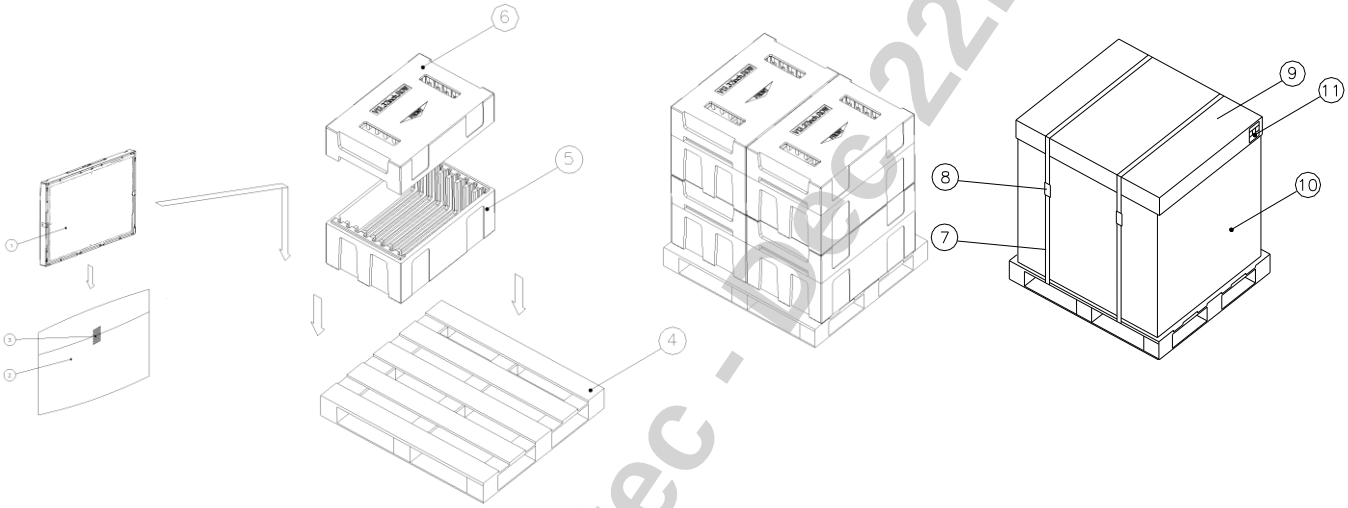
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark
Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 12 pcs

b) Box Size : 542mm×934mm×450mm



NO.	DESCRIPTION	MATERIAL
1	LCM	-
2	BAG	AL
3	TAPE	OPP
4	PALLET	PLYWOOD_1140X990X117.5
5	PACKING, BOTTOM	EPS
6	PACKING, TOP	EPS
7	BAND	PP
8	BAND, CLIP	CLIP 18MM
9	Angle Cover	PAPER
10	Angle Packing	PAPER
11	LABEL	PP

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

1) You must mount a module using holes arranged in rear side.
2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
4) You should adopt radiation structure to satisfy the temperature specification.
5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.
(Some cosmetics are detrimental to the polarizer.)
7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
9) Do not open the case because inside circuits do not have sufficient strength.
10) System frame should not have an interference with panel which can cause LC Leakage/Panel Crack due to the contraction of system frame at low temperature condition or panel damage by any other circumstances.

9-2. Operating Precautions

1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
2) Brightness depends on the temperature.(In higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
7) A screw which is fastened up the steels should be a machine screw.(if not, it causes metallic foreign material and deal LCM a fatal blow)
8) Please do not set LCD on its edge.
9) When LCMs are used for public display, defects such as Yogore & image sticking can not be guaranteed.
10) LCMs cannot support "Interlaced Scan Method"
11) When this reverse model is used as a forward-type model (PCB on top side) or a Portrait-type mode at storage and operation, LGD can not guarantee any defects of LCM.
12) Please conduct image sticking test after 2-hour aging with Rolling Pattern at normal temperature.(25~40℃)

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions For Strong Light and Hazardous Materials Exposure

Strong light exposure causes degradation of polarizer and color filter.
The LCM should be avoided direct contact with hazardous materials such as sulfur, acetic acid, chlorine, etc. These materials may cause chemical reaction such as sulfurization, corrosion, discoloration, etc.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
2) The polarizer surface should not come in contact with any other object.
   It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions For Protection Film

1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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